

Modeling and analysis of multilevel spiral inductors for RFICs

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An accurate CAD-oriented modeling methodology for multilevel rectangular spiral inductors with enhanced inductance and/or quality factor is presented. The method is based on a distributed equivalent circuit model whose elements are computed using Spectral Domain and Partial Element Equivalent Circuit (PEEC) techniques. Examples of typical two layer spirals in BiCMOS and CMOS technologies are presented to illustrate the accuracy of the model as well as the enhanced performance of the multilevel structures.

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